Notice of References Cited	Application/Control No. 10/051,405	Applicant(s)/Patent Under Reexamination BRANSON ET AL.	
	Examiner Anil Khatri	Art Unit 2193	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
X	A	US-6,874,143	03-2005	Murray et al.	717/173
X	B	US-6,889,158	05-2005	Penov et al.	702/119
X	C	US-6,202,207	03-2001	Donohue, Seamus Brendan	717/173
X	D	US-5,887,192	03-1999	Nishio, Nobuhiko	710/7
X	E	US-4,558,413	12-1985	Schmidt et al.	707/203
X	F	US-5,410,703	04-1995	Nilsson et al.	717/168
X	G	US-5,848,154	12-1998	Nishio et al.	705/51
X	H	US-5,867,714	02-1999	Todd et al.	717/172
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Choi et al, "Stack allocation and synchronization optimization for Java using escape analysis", ACM Trans on Prog. Lang and Sys. vol. 25, no. 6, pp 876-910, November 2003
	V	Veitch et al, "Robust synchronization of software clocks across the internet", ACM IMC, pp 219-232, Oct. 2004
	W	Laventhal, "A constructive approach to reliable synchronization code", IEEE, pp 194-202, 1979
	X	Lee et al, "Reducing coherence overhead of barrier synchronization in software DSMS", IEEE SC, pp 1-18, 1998

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

U.S. Patent and Trademark Office
PTO-892 (Rev. 01-2001)   **Notice of References Cited**   Part of Paper No. 20050621